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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/828,896	04/20/2004	Clement B. Edgar III	PA716D1C1	5845

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QUALCOMM INCORPORATED
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EXAMINER	
HOM, SHICK C	

ART UNIT	PAPER NUMBER
2471	

NOTIFICATION DATE	DELIVERY MODE
11/26/2010	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

us-docketing@qualcomm.com

Office Action Summary	Application No.	Applicant(s)	
	10/828,896	EDGAR ET AL.	
	Examiner	Art Unit	
	SHICK C. HOM	2471	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *30 September 2010*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-25* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☒ Claim(s) *10-17 and 22* is/are allowed.

6)☒ Claim(s) *1-9, 18-21, 23-25* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments with respect to claims 1-25 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in

order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

3. Claims 1-9, 18-21, and 23-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lin et al. (2001/0006517) in view of Childress et al. (4,905,234).

Regarding claims 1-9, 18-21, and 23-25:

Lin et al. disclose the telephone apparatus, comprising a transceiver that communicates with a central station; a plurality of desksets; and an interface bus that permits said desksets to communicate with said transceiver by exchanging packets with the transceiver as in claim 1; wherein said interface bus comprises a pair of conductors as in claim 6; and wherein the central station corresponds to a base station within an access network that is configure to provide wireless communications services to each of the plurality of desksets through the transceiver as in claims 23-24 (Fig. 5 shows transceiver 402 communicating with central station (413, 414); the desksets 416, the interface bus, i.e. pair of conductors (501, 412) that permits the desksets 416 to communicate with transceiver (402), including the wireless communications

services (410, 411) between the desksets (416) and the base station; further paragraphs 0098-0099 recite the exchange of data being packet data).

Further, Lin et al. disclose wherein each deskset has a different pre-assigned time-out period as in claims 18, 21, and wherein each of the plurality of desksets exchanging packets with the transceiver is configured to send data to the transceiver in a different manner from each other deskset based on an associated address of the deskset as in claim 20 (paragraphs 00007-0008 which recite the user access to the base station being via assigned time slots using time division multiplexing clearly reads on pre-assigned time-out period because each user uses an assigned time period, i.e. time slot, to communicate and reads on exchanging packets in a different manner from each other deskset based on the deskset).

Lin et al. disclose all the subject matter of the claimed invention with the exception of each packet including source, destination and error checking information as in claim 1; each packet comprising: an address (ADDR) byte that includes source and destination addresses of the packet; a command (CMD) byte; an argument (ARG); and a block check character (BCC) for error checking as in claim 2; wherein said BCC is produced by a longitudinal parity check as in claim 3; wherein said BCC is

produced by a cyclic redundancy check as in claim 4; wherein each packet further comprises a start of header (SOH) byte that indicates the start of the packet as in claim 5; wherein said interface bus comprises an unshielded twisted pair as in claim 7; wherein said interface bus comprises an EIA-485 interface as in claim 8; wherein a media access layer of said interface bus is carrier sense multiple access with collision detect as in claim 9; wherein source information included in each packet identifies a given deskset among that the plurality of desksets that is sending the packet as in claim 19.

Childress et al. from the same or similar fields of endeavor teach that it is known to provide each packet including source, destination and error checking information; each packet comprising: an address (ADDR) byte that includes source and destination addresses of the packet; a command (CMD) byte; an argument (ARG); and a block check character (BCC) for error checking; wherein said BCC is produced by a longitudinal parity check; wherein said BCC is produced by a cyclic redundancy check; wherein each packet further comprises a start of header (SOH) byte that indicates the start of the packet; wherein source information included in each packet identifies a given deskset among that the plurality of desksets that is sending the packet (Fig. 5a shows the packet including source, destination

and error checking information; i.e. the address (ADDR) byte that includes source and destination addresses of the packet (196, 198); a command (CMD) byte (190); an argument (ARG) (192, 194); and a block check character (BCC) for error checking (206); and Fig. 21 shows the use of the start of header that indicates the start of the packet; whereby the use of the source address clearly reads on the source information that identifies a given deskset among that the plurality of desksets that is sending the packet); although Childress et al. did not recite the BCC being produced by a longitudinal parity check; use of an unshielded twisted pair; the EIA-485 interface; and wherein the media access layer of said interface bus is carrier sense multiple access with collision detect; the examiner takes official notice that the use of longitudinal parity check; of an unshielded twisted pair; the EIA-485 interface; and wherein the media access layer of said interface bus is carrier sense multiple access with collision detect is well-known in the art.

Thus, it would have been obvious to the person having ordinary skill in the art at the time the invention was made to provide each packet including source, destination and error checking information; each packet comprising: an address (ADDR) byte that includes source and destination addresses of the packet; a command (CMD) byte; an argument (ARG); and a block

check character (BCC) for error checking; wherein said BCC is produced by a longitudinal parity check; wherein said BCC is produced by a cyclic redundancy check; wherein each packet further comprises a start of header (SOH) byte that indicates the start of the packet; wherein source information included in each packet identifies a given deskset among that the plurality of desksets that is sending the packet as taught by Childress et al. in the communications system and apparatus of Lin et al.

The packet including source, destination and error checking information; each packet comprising: an address (ADDR) byte that includes source and destination addresses of the packet; a command (CMD) byte; an argument (ARG); and a block check character (BCC) for error checking; wherein said BCC is produced by a longitudinal parity check; wherein said BCC is produced by a cyclic redundancy check; wherein each packet further comprises a start of header (SOH) byte that indicates the start of the packet; wherein source information included in each packet identifies a given deskset among that the plurality of desksets that is sending the packet can be implemented by using the packet including source, destination and error checking information; each packet comprising: an address (ADDR) byte that includes source and destination addresses of the packet; a command (CMD) byte; an argument (ARG); and a block check

character (BCC) for error checking; wherein said BCC is produced by a longitudinal parity check; wherein said BCC is produced by a cyclic redundancy check; wherein each packet further comprises a start of header (SOH) byte that indicates the start of the packet; wherein source information included in each packet identifies a given deskset among that the plurality of desksets that is sending the packet of Childress et al. for the packet of Lin et al. The motivation for using the packet having source, destination and error checking information; i.e. comprising an address (ADDR) byte that includes source and destination addresses of the packet; a command (CMD) byte; an argument (ARG); and a block check character (BCC) for error checking; wherein said BCC is produced by a longitudinal parity check; wherein said BCC is produced by a cyclic redundancy check; wherein each packet further comprises a start of header (SOH) byte that indicates the start of the packet; wherein source information included in each packet identifies a given deskset among that the plurality of desksets that is sending the packet buffers and PHO extractor as taught by Childress et al. in the communication system and apparatus of Lin et al. being that it provides more efficiency for the system design since the system design uses well-known and standard definition of packet as unit of data for transmission in the system and apparatus.

Allowable Subject Matter

4. Claims 10-17 and 22 are allowed.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
O'Hagan discloses speech recognition system and method for employing the same.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to SHICK C. HOM whose telephone number is (571)272-3173. The examiner can normally be reached on Mon-Thurs.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Pham Chi can be reached on 571-272-3179. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Chi H Pham/
Supervisory Patent
Examiner, Art Unit 2471

SH